

Reflective multi-layer etching for repairing clear defect on EUV masks using FIB

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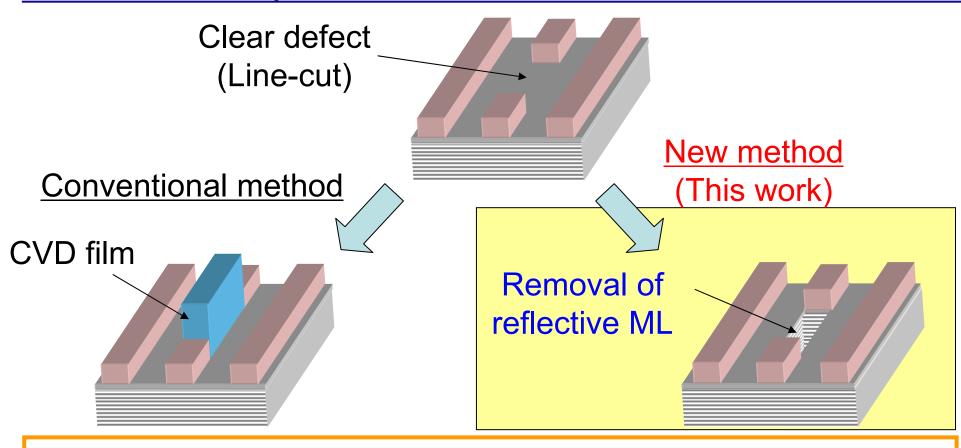
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Semiconductor Leading Edge Technologies, Inc.

A IRAI How to repair clear defects on EUV mask? Selete



Issues of conventional process of forming CVD films are:

Lack of the EUV-light shielding capability.

=> Thick film will cause large shadowing effect.

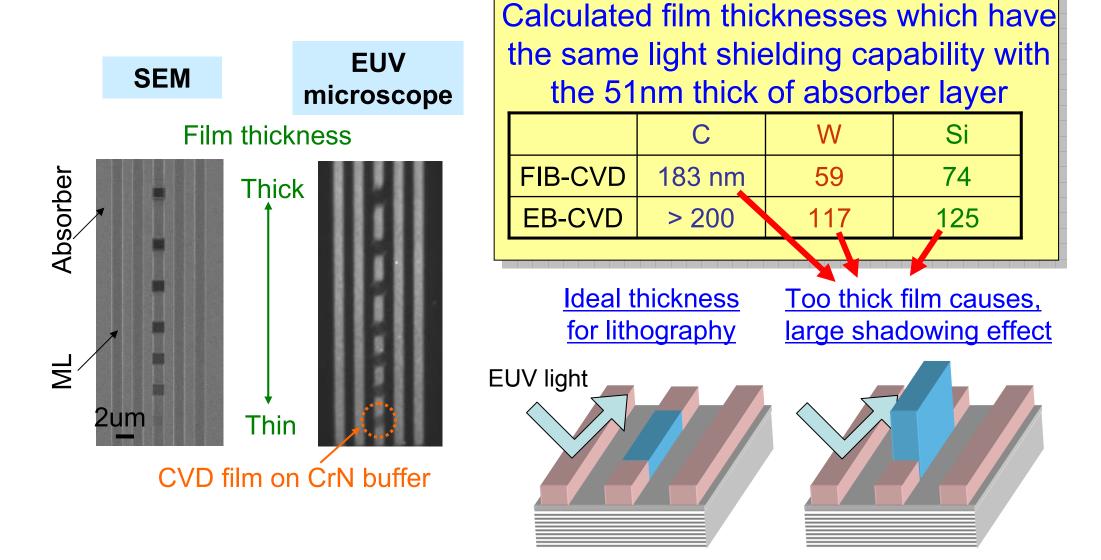
Lack of the cleaning selectivity over contamination.

=> Wafer printability will be changed.



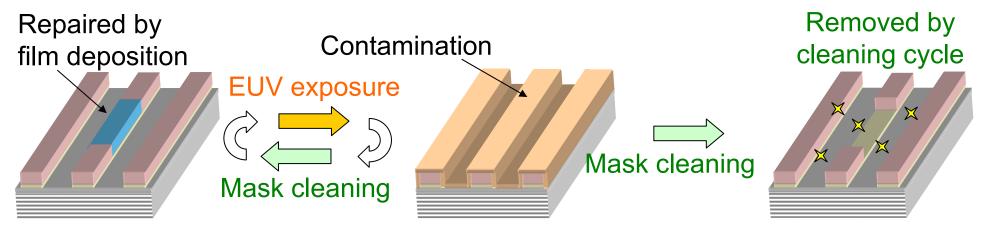
Light shielding capability of CVD films



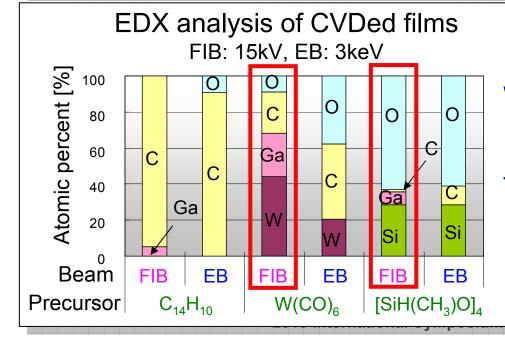








CVD films are required extremely high cleaning selectivity.

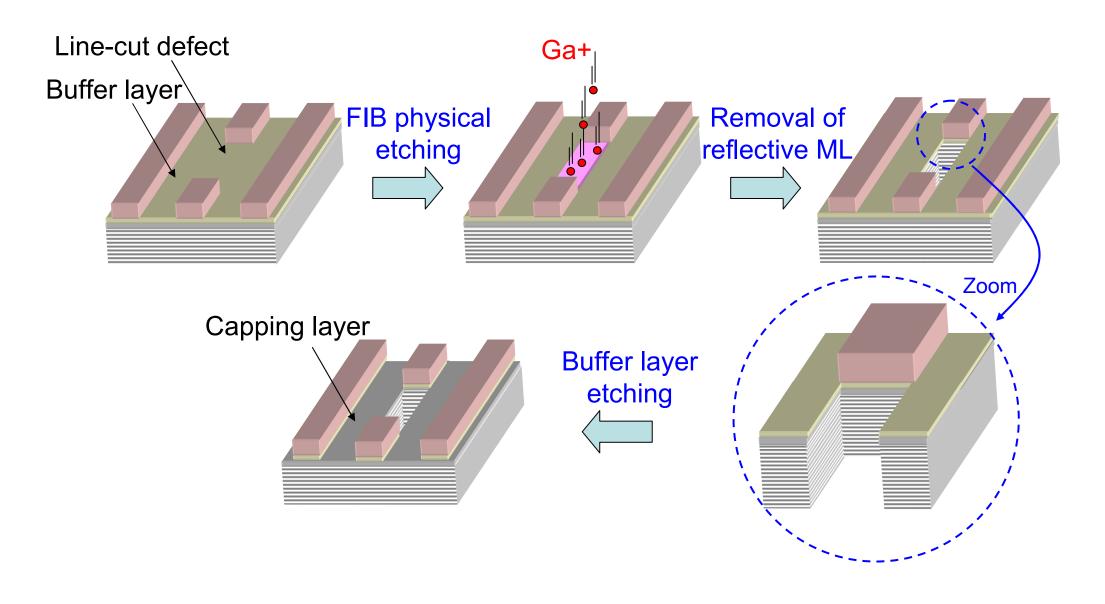


W or Si based FIB-CVD films did not loss the thicknesses against three types of dry cleaning process (VUV, Pure O_3 and H-radical).



Overview of the repair process







Experiment systems



Repair tool and condition

Tool: SIR-7 (SII NanoTechnology Inc.)

for 65 and 45 nm node

Ion: Ga ion

Vacc: 15 kV

Etching gas: none



Exposure tool and condition

Tool: SFET*

Exposure optics: NA: 0.3 (central obscuration 30%)

Sigma (inner/outer): 0.3/ 0.7

Magnification: 1/5

Incident angle: 6 deg.

Resist: SSR4**

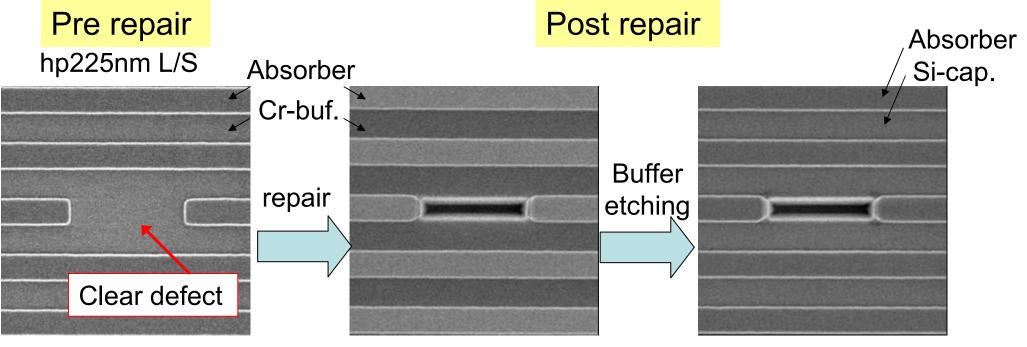
* SFET: Small Field Exposure Tool

** Selete Standard Resist 4

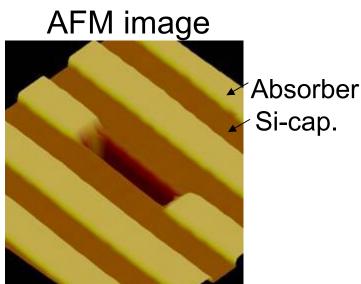


Images of pre/ post repaired process





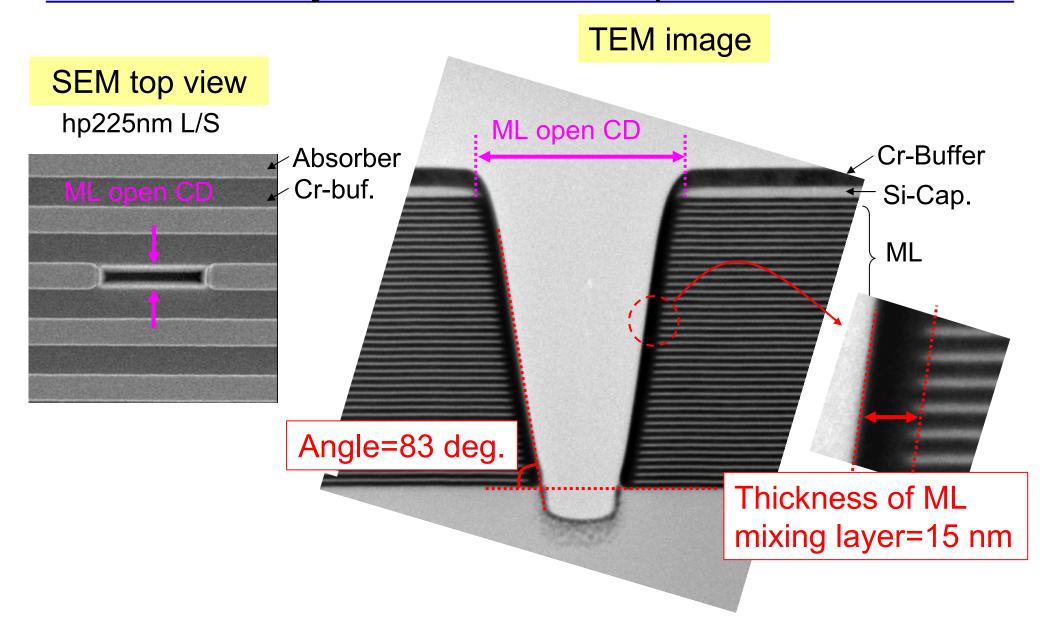
- The repair process performed well.
- The etched ML did not show any obvious size change after buffer etching.
- No buffer residue was observed on the Si-cap. layer.





Analysis of ML etched pattern

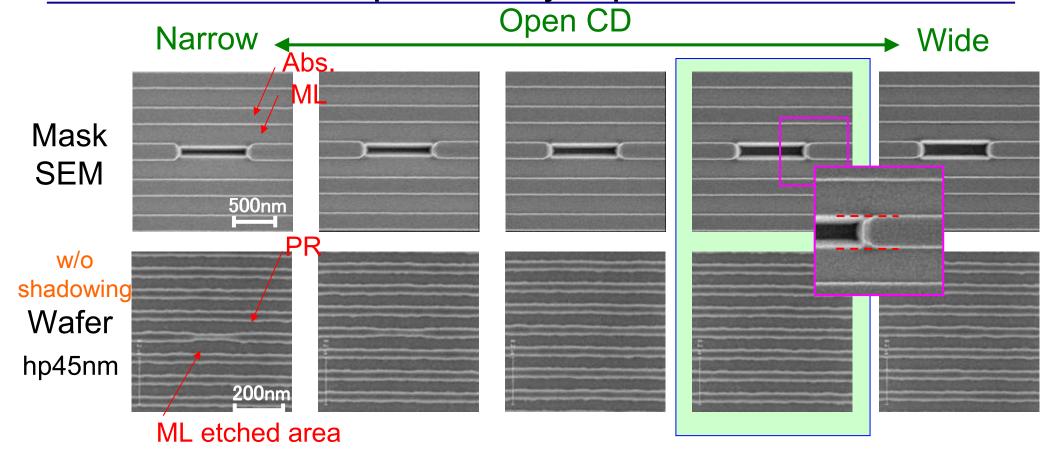






Wafer printability /hp225nm





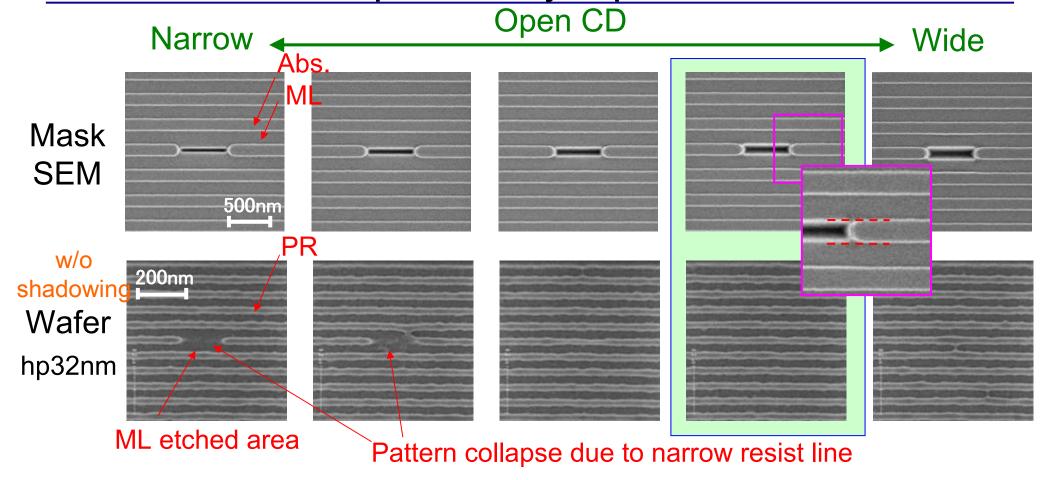
SFET exposure results showed that;

- the ML etched area behaved as low reflection area
- the printed CDs were proportional to the ML open CDs
- the best performance was obtained to fit the ML open CD to the absorber CD



Wafer printability /hp160nm





The exposure result showed the same tendency of hp45nm.

- the best performance was obtained to fit the ML open CD to the absorber CD



Simulation condition



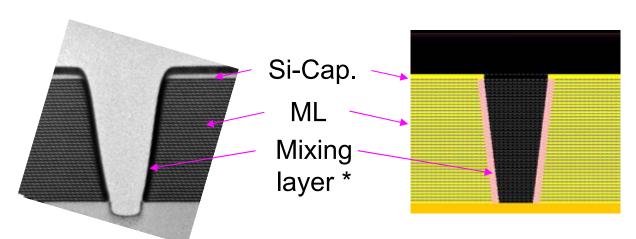
Simulator: EM-Suite TM (Panoramic Technology)

Exposure condition: Same condition as SFET

CD calculation: Threshold method

TEM image

Simulation Model

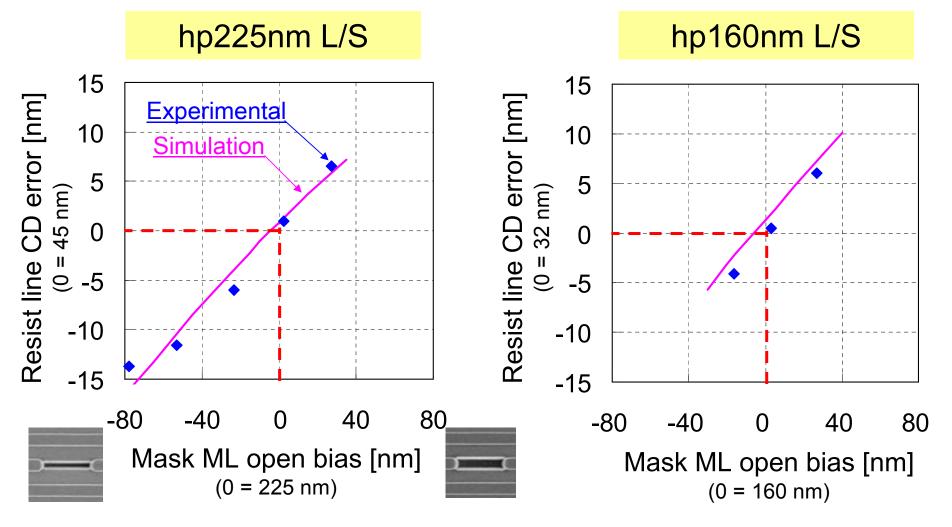


* n, k values were calculated from the optical index of Si and Mo



Mask ML open CD vs. Resist line CD





- Simulation model and results agree well with the experimental results.
- The resist line CDs are slightly larger than the one-fifth of ML open CD.

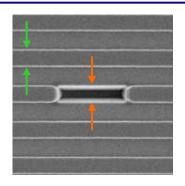


Defocus characteristic

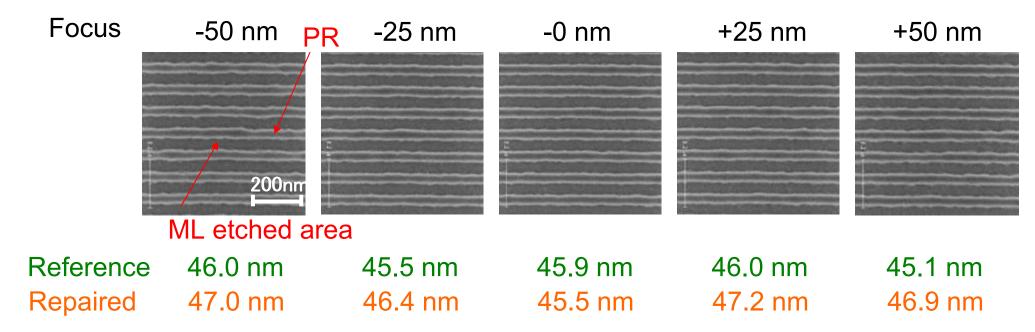


Mask design: hp225nm L/S

Abs. line CD = 224.7 nmML open CD = 227.3 nm



Wafer printed images

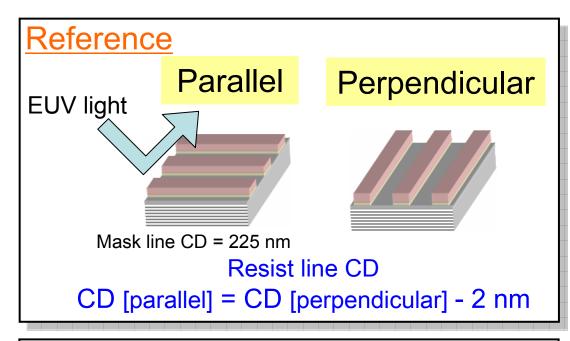


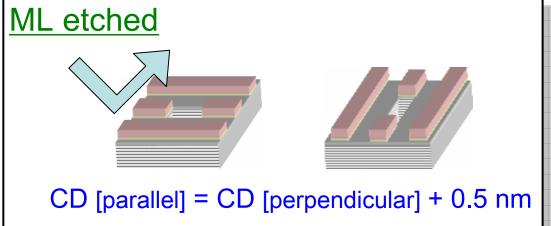
The defocus characteristic was excellent, larger than 50 nm.



Shadowing effect of hp225nm pattern

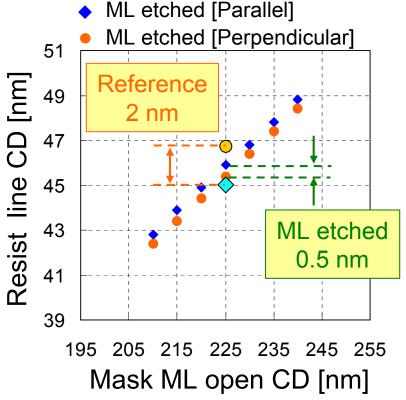






Mask ML open CD vs. printed CD

- Reference [Parallel]
- Reference [Perpendicular]



The ML etching method will be requires its repair be done under the consideration of the shadowing effect.



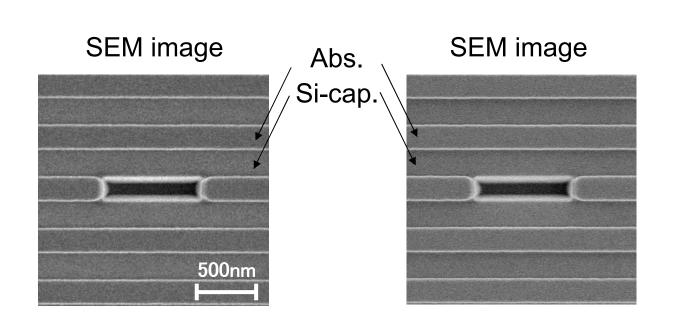
Cleaning process durability

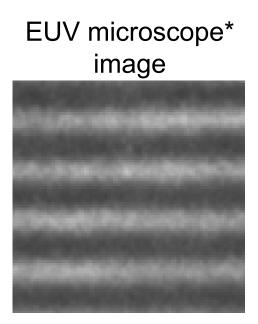


Cleaning process
(APM=> Rinse=> Dry) x 5 cycles

Pre cleaning process

Post cleaning process





No cleaning damage was observed.

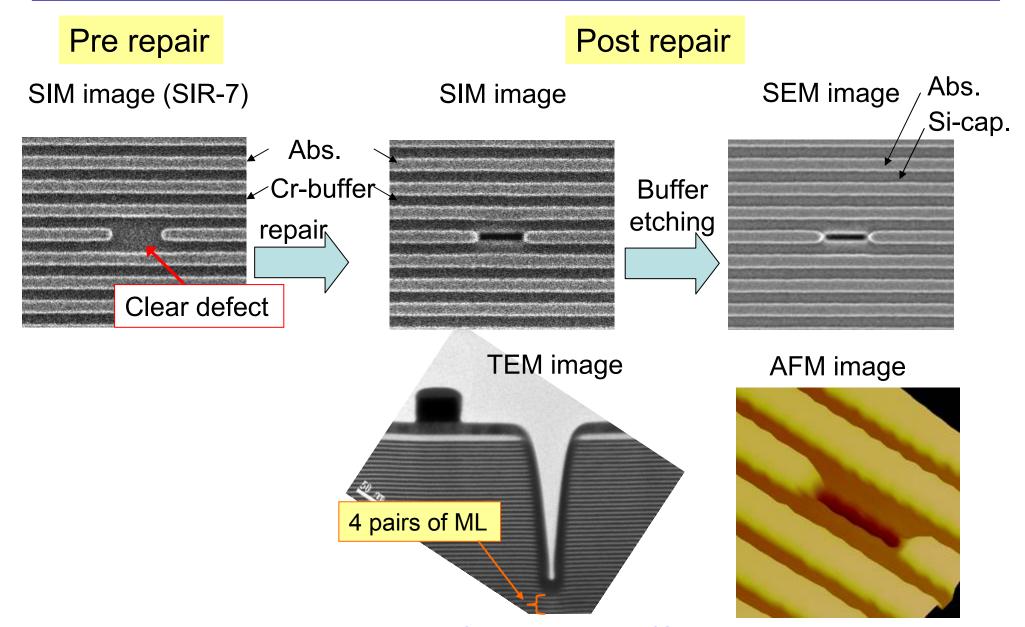
* Center for EUV Lithography, LASTI, University of Hyogo

"Actinic Mask Inspection using an Extreme Ultraviolet Microscope" K. Takase et al., Proc. SPIE Vol. 7379 (2009).



Application to hp88nm L/S pattern







Summary



ML etching performance

- ✓ The ML underlying the line-cut defects were successfully removed by FIB physical etching process.
- ✓ The sidewall angle was 83 degree and the thickness of the mixing layer was about 15 nm.

Wafer printing performance

- ✓ The wafer printed CDs were proportional to the ML etched CDs.
- ✓ The printing performance of ML etched pattern was not sensitive to focus error.
- ✓ The shadowing effect of the ML etched pattern was estimated to be smaller than that of the absorber pattern.





Acknowledgement

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